

# COMPUTER DATA LINES

Fig 1: Computer Data Lines Circuit (1 of 2)

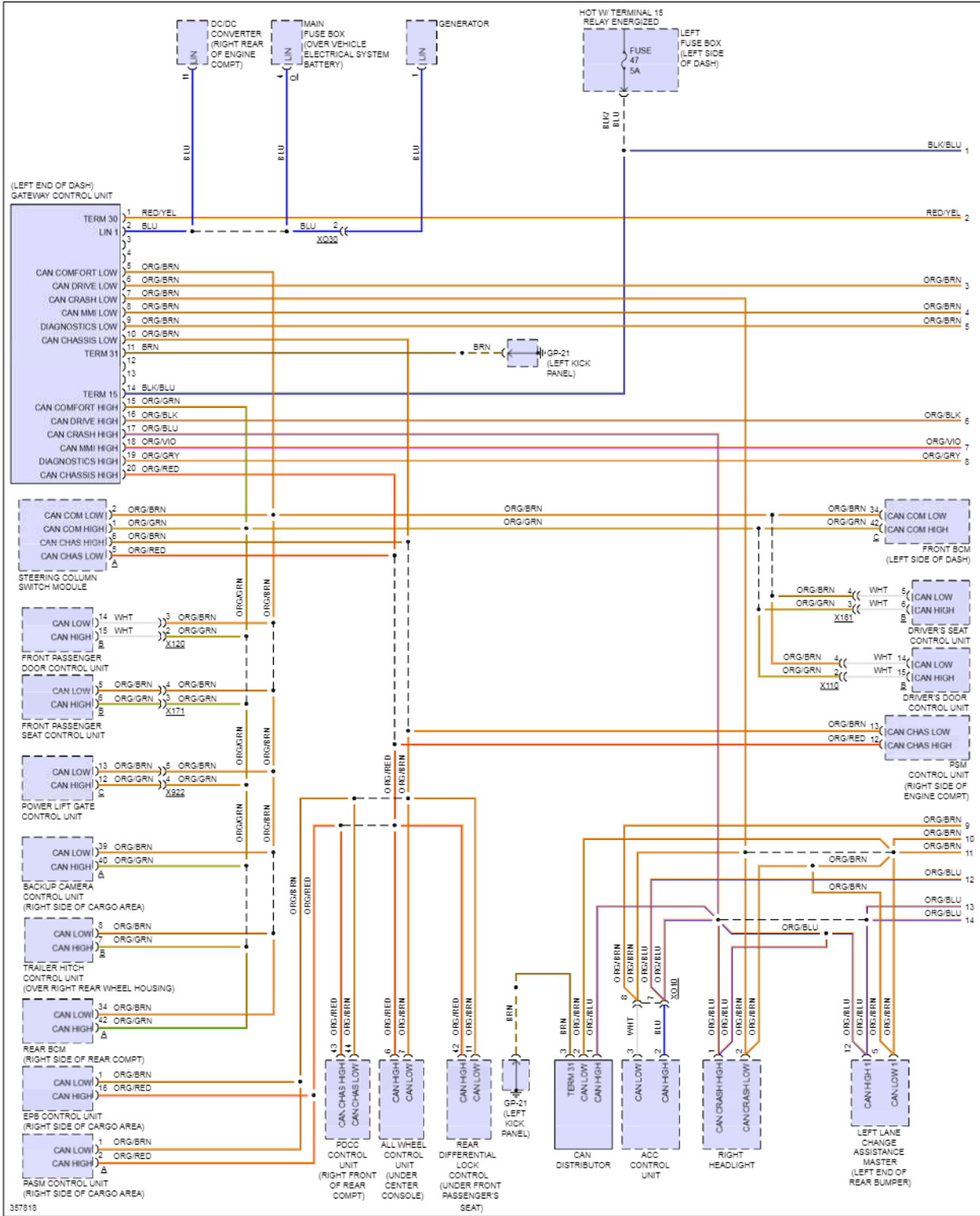


Fig 2: Computer Data Lines Circuit (2 of 2)

